

Electronic Patent Application Fee Transmittal

Application Number:	10707797			
Filing Date:	13-Jan-2004			
Title of Invention:	Integrated Redundancy Architecture and Method for Providing Redundancy Allocation to an Embedded Memory System			
First Named Inventor/Applicant Name:	Wayne F. Ellis			
Filer:	Morgan Heller/Karen Jeffer			
Attorney Docket Number:	BUR920030151US1			
Filed as Large Entity				
Utility under 35 USC 111(a) Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Request for continued examination	1801	1	810	810
Total in USD (\$)				810